

**ABSTRACT**

A system is provided for synchronizing a PSTN clock and a DSL clock. The system comprises a PSTN interface for transmitting and receiving voiceband samples, a data DSL transceiver for modulating and demodulating data to and from DSL samples, a  
5 synchronization circuit for synchronizing the voiceband samples and the data samples, and a converter for converting the synchronized voiceband and DSL samples between analog and digital formats. The synchronization circuit synchronizes the voiceband and the data samples for conversion by the same converter.

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